

Electronic Acknowledgement Receipt

EFS ID:	1053971
Application Number:	10696865
Confirmation Number:	8071
Title of Invention:	Method and apparatus for using FPGA technology with a microprocessor for reconfigurable, instruction level hardware acceleration
First Named Inventor:	Andreas C. Doering
Customer Number:	47052
Filer:	Kelvin M. Vivian/Jinny Nguyen
Filer Authorized By:	Kelvin M. Vivian
Attorney Docket Number:	RPS920030110US1
Receipt Date:	22-MAY-2006
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Application Type:	Utility
International Application Number:	

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		RPS920030110US1Amend.pdf	3204232	yes	21

	Multipart Description		
	Doc Desc	Start	End
	Amendment - After Non-Final Rejection	1	1
	Specification	2	10
	Claims	11	19
	Applicant Arguments/Remarks Made in an Amendment	20	21

Warnings:

Information:

2	Transmittal letter	RPS920030110US1Transm.pdf	234147	no	1
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Warnings:

Information:

Total Files Size (in bytes):	3438379
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.